

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
)  
Kiminori WATANABE et al. ) Prior Group Art Unit: 2815  
)  
Div. of Application No.: 10/277,744 ) Prior Examiner: J. Jackson, Jr.  
(filed October 23, 2002) )  
)  
Filed: Herewith )  
)  
For: LATERAL HIGH-BREAKDOWN- )  
VOLTAGE TRANSISTOR )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicants bring to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed concurrently with the above-referenced divisional application.

Copies of the listed documents were previously submitted by Applicants or cited by the Examiner in prior application Nos. 10/277,744, filed October 23, 2002 and 09/746,223, filed December 26, 2000, upon which Applicants rely for the benefits provided in 35 U.S.C. § 120. Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim

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in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: December 31, 2003

By: Bryan S. Latham  
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## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.2480-02	Div of Appln. No.	10/277,744
Applicant	Kiminori WATANABE et al.		
Filing Date	Herewith	Group:	2815

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
	4,589,004	05-1986	Yasuda et al.			
	6,081,002	06-2000	Amerasekera et al.			
	6,194,761	02-2001	Chiozzi et al.			
	6,242,787	06-2001	Nakayama et al.			
	6,392,275	05-2002	Jang			

FOREIGN PATENT DOCUMENTS						
Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	J.A. Appels, et al., "High Voltage Thin Layer Devices (Resurf Devices), Philips Research Laboratories, Proc. IEDM, pp. 238-241, (1979).

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce